

AMENDMENTS TO THE CLAIMS:

1. (Currently amended) A fixed point data generating circuit which receives a plurality of floating point data and ~~which~~ converts said received plurality of floating point data into respective fixed point data, said fixed point data generating circuit comprising:

a reference data determining unit ~~which determines~~ for determining a reference floating point data from said received plurality of floating point data;

an exponent part subtractor unit ~~which obtains a difference for obtaining the differences between each of the values of the exponent parts of said plurality of inputted the received floating point data which are not determined as said reference floating point data and~~ a value of an exponent part of said reference floating point data;

a shifting unit ~~which shifts~~ for shifting a mantissa part of each of said plurality of floating point data by ~~said the~~ difference obtained by ~~said exponent part subtracting unit for the corresponding floating point data; and~~

a bit extracting unit ~~which extracts for extracting~~ a predetermined number of ~~the most significant bits of said each shifted~~ mantissa part ~~shifted by said shifting unit as fixed point data,~~

wherein when an overflow occurs in said bits extracted by said bit extracting unit as said fixed point data, said extracted bits are accepted as representing the maximum value.

2. (Previously presented) A fixed point data generating circuit as set forth in claim 1, wherein said reference data determining unit comprises a maximum value detecting circuit which detects the maximum value from among the values of said plurality of floating point

data, and said reference floating point data is the data having the detected maximum value.

3. (Previously presented) A fixed point data generating circuit as set forth in claim 1, wherein said reference data determining unit comprises a minimum value detecting circuit which detects the minimum value from among the values of said plurality of floating point data, and said reference floating point data is the data having the detected minimum value.

4. (Previously presented) A fixed point data generating circuit as set forth in claim 1, wherein said reference data determining unit comprises an average value calculating circuit which calculates an average value of the values of said floating point data, and said reference floating point data is data having the average value.

5-9. (Canceled)

10. (Currently amended) A computer-implemented method for generating fixed point data in which a plurality of inputted floating point data are converted into respective fixed point data, said computer-implemented method comprising:

determining a reference floating point data from said plurality of inputted floating point data;

obtaining ~~a difference~~ the differences between ~~each of~~ the values of the exponent parts of ~~said plurality of the~~ inputted floating point data which are not determined as said reference floating point data and a value of an exponent part of said reference floating point data;

shifting a mantissa part of each of said plurality of floating point data by ~~said the~~
~~difference between each of said values of said exponent parts of said plurality of inputted~~
obtained for the corresponding floating point data; ~~and said value of said exponent part of~~
~~said reference floating point data;~~ and

extracting a predetermined number of ~~the most significant bits from said of each~~
shifted mantissa part ~~shifted by said difference~~ as fixed point data,

wherein in extracting a predetermined number of bits, when an overflow occurs in
said extracted bits, said extracted bits are accepted as representing the maximum value.

11. (Original) A method for generating fixed point data as set forth in claim 10, wherein said reference floating point data is the maximum data among said plurality of floating point data.

12. (Original) A method for generating fixed point data as set forth in claim 10, wherein said reference floating point data is the minimum data among said plurality of floating point data.

13. (Previously presented) A method for generating fixed point data as set forth in claim 10, wherein said reference floating point data is the average data of said plurality of floating point data.

14-18. (Canceled)

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19. (Previously presented) A method for generating fixed point data as set forth in claim 10, further comprising utilizing the fixed point data.
20. (Previously presented) A method for generating fixed point data as set forth in claim 19, wherein the fixed point data is utilized in a Code Division Multiple Access system.